

17-38 stand rejected by the August 30, 2002 Final Office Action. Reexamination and reconsideration of the application, are respectfully requested.

Claims 1-5, 7, 11, 14, 17-33 and 35 are rejected under 35 U.S.C. § 103(a) as being unpatentable over the related art shown in Fig. 1 of the application in view of U.S. Patent No. 6,344,883 to Yamada et al. (hereinafter the '883 patent) and claims 6, 8-10, 34 and 36-38 are rejected under 35 U.S.C. § 103(a) as being unpatentable over the related art in view of the '883 patent in combination with U.S. Patent No. 6,061,117 to Horie et al. (hereinafter the '117 patent). These rejections are respectfully traversed.

Claims 1-38 are allowable over the cited references, in that none of the cited references, specifically the '883 patent nor the '117 patent, recites a combination of elements, including, for example, "dielectric frame distorting electric field applied to said liquid crystal layer" or "dielectric protrusions are formed in the pixel region". None of the cited references including the '883 patent and the '117 patent, singly or in combination, teaches or suggests at least these features of the claimed invention. Accordingly, Applicants respectfully submit that independent claims 1 and 30, and claims 2-29 and 31-38, which depend there from are allowable over the cited references.

As is best understood, the photoresist material described in Yamada simply provides a spacer. See, for example column 25, lines 32-34, which describe the photoresist as a spacer 65. However, contrary to the Examiner's continued assertion, there is no teaching in Yamada (the '883 patent) that the photoresist has any affect to the electric field within the liquid crystal display device. Moreover, no reference that would teach or suggest any dielectric properties of any photoresist have been provided. Therefore, Applicants submit that the Examiner has not met his burden to present a prima facie case of obviousness under 35 U.S.C. § 103 and respectfully request withdrawal of the rejections based on Yamada (the '883 patent).

Applicants therefore believe the foregoing amendments place the application in condition for allowance and early, favorable action is respectfully solicited.

Should the Examiner deem that a telephone conference would further the prosecution of this application, the Examiner is invited to call the undersigned representative at (202) 496-

7500.

If these papers are not considered timely filed by the U.S. Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. § 1.136, and any additional fees required under 37 C.F.R. § 1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to Deposit Account No. 50-0911. A duplicate copy of sheet is enclosed.

Dated: December 2, 2002

Respectfully submitted,

By 

Rebecca Goldman Rudich

Registration No.: 41,786

MCKENNA LONG & ALDRIDGE LLP

1900 K Street, N.W.

Washington, DC 20006

(202) 496-7500

Attorneys for Applicant

Attachment:

Exhibit 1 (showing changes to the claims)



30827

PATENT TRADEMARK OFFICE

**Version With Markings to Show Changes Made**

12. The A multi-domain liquid crystal display device, comprising: according to claim  
2,

first and second substrates facing each other;  
a liquid crystal layer between said first and second substrates;  
a plurality of gate bus lines arranged in a first direction on said first substrate and a  
plurality of data bus lines arranged in a second direction on said first substrate to define a pixel  
region;  
a pixel electrode in said pixel region;  
a dielectric frame in a region other than a region where said pixel electrode is formed,  
said dielectric frame distorting electric field applied to said liquid crystal layer;  
a common electrode on said second substrate;  
an alignment layer on at least one substrate between said first and second substrates,  
a gate insulator over said whole first substrate;  
a passivation layer on said gate insulator over said whole first substrate, the passivation  
layer having an electric field including window inside;  
a light shielding layer on said second substrate;  
a color filter layer on said light shielding layer; and  
an over coat layer on said color filter layer,  
~~wherein said passivation layer has an electric field inducing window inside of itself.~~

13. The A multi-domain liquid crystal display device, comprising: according to claim  
2,

first and second substrates facing each other;

a liquid crystal layer between said first and second substrates;  
a plurality of gate bus lines arranged in a first direction on said first substrate and a  
plurality of data bus lines arranged in a second direction on said first substrate to define a pixel  
region;  
a pixel electrode in said pixel region;  
a dielectric frame in a region other than a region where said pixel electrode is formed,  
said dielectric frame distorting electric field applied to said liquid crystal layer;  
a common electrode on said second substrate;  
an alignment layer on at least one substrate between said first and second substrates,  
a gate insulator over said whole first substrate, the gate insulator having an electric field  
including window inside;  
a passivation layer on said gate insulator over said whole first substrate;  
a light shielding layer on said second substrate;  
a color filter layer on said light shielding layer; and  
an over coat layer on said color filter layer.  
~~wherein said gate insulator has an electric field inducing window inside of itself.~~

15. ~~The~~ A multi-domain liquid crystal display device, comprising: according to claim  
2,

first and second substrates facing each other;  
a liquid crystal layer between said first and second substrates;  
a plurality of gate bus lines arranged in a first direction on said first substrate and a  
plurality of data bus lines arranged in a second direction on said first substrate to define a pixel  
region;

a pixel electrode in said pixel region;  
a dielectric frame in a region other than a region where said pixel electrode is formed,  
said dielectric frame distorting electric field applied to said liquid crystal layer;  
a common electrode on said second substrate;  
an alignment layer on at least one substrate between said first and second substrates,  
a gate insulator over said whole first substrate;  
a passivation layer on said gate insulator over said whole first substrate;  
a light shielding layer on said second substrate;  
a color filter layer on said light shielding layer; and  
an over coat layer on said color filter layer, the over coat layer having an electric field  
including window inside.

~~wherein said color filter layer has an electric field inducing window inside of itself.~~

16. The multi-domain liquid crystal display device, ~~comprising: according to claim 2,~~  
first and second substrates facing each other;  
a liquid crystal layer between said first and second substrates;  
a plurality of gate bus lines arranged in a first direction on said first substrate and a  
plurality of data bus lines arranged in a second direction on said first substrate to define a pixel  
region;

a pixel electrode in said pixel region;  
a dielectric frame in a region other than a region where said pixel electrode is formed,  
said dielectric frame distorting electric field applied to said liquid crystal layer;  
a common electrode on said second substrate;  
an alignment layer on at least one substrate between said first and second substrates,

a gate insulator over said whole first substrate;

a passivation layer on said gate insulator over said whole first substrate;

a light shielding layer on said second substrate;

a color filter layer on said light shielding layer; and

an over coat layer on said color filter layer,

wherein said over coat layer has an electric field inducing window inside of itself.